

<b>Notice of References Cited</b>			Application No. <b>09 039,106</b>		Applicant(s) <b>Rozman</b>	
			Examiner <b>Riley</b>		Group Art Unit <b>2838</b>	Page <b>1</b> of <b>1</b>

  

U.S. PATENT DOCUMENTS					
*	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
* A	5,303,138	4/94	Rozman	363	21
B					
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*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
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Q						
R						
S						
T						

  

NON-PATENT DOCUMENTS		
*	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
* U	"Current Controlled Synchronous Rectification", by B. Acker, C. C. R. Sullivan, S. R. Sanders, IEEE pp 185-191, May 94	
* V	"High Efficiency DC-DC Converter" by I. D. Jitaru, S. Cocina, IEEE, pp. 638-644 May 1994	
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\* A copy of this reference is not being furnished with this Office action.  
(See Manual of Patent Examining Procedure, Section 707.05(a).)